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<b>From:</b>	Robert D. Atkins	602-229-5690	602-229-5311
<b>Re:</b>	USSN: 10/767,384		

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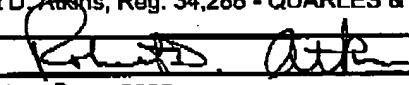
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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10767,384	
	Filing Date	January 28, 2004	
	First Named Inventor	Badredin Fatemizadeh	
	Art Unit	2826	
	Examiner Name	Wilson, Scott R.	
Total Number of Pages in This Submission	10	Attorney Docket Number	112518.00006

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<b>SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT</b>		
Firm or Individual name	Robert D. Atkins, Reg. 34,288 - QUARLES & BRADY STREICH LANG LLP	
Signature		
Date	November 21, 2005	

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Badredin Fatemizadeh et al.  
Serial Number : 10/767,384  
Date of Filing : January 28, 2004  
Title : LOW COST DIELECTRIC ISOLATION METHOD  
FOR INTEGRATION OF VERTICAL POWER  
MOSFET AND LATERAL DRIVER DEVICES  
Art Unit : 2826  
Examiner : Wilson, Scott R.  
Confirmation No. : 4751  
USPTO Cust. No. : 26707  
Attorney Docket No. : 112518.00006

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RESPONSE UNDER 37 C.F.R. §1.111

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Commissioner for Patents  
P.O. Box 1450  
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Sir:

In response to the Office Action dated August 24, 2005,  
Applicant(s) respectfully request the Examiner to reconsider and  
further examine the Application in view of the amendments and/or  
comments as set forth below.

Amendments to the Specification: None.

Amendments to the Claims: Begin on page 2.

Amendments to the Drawings: None.

Remarks: Begin on page 7.